

AMENDMENTS TO THE CLAIMS

This listing of claims replaces all prior versions of claims in the application.

Listing of Claims:

Claim 1 (Canceled).

Claim 2 (Previously Presented): A method for controlling a video signal circuit including a preamplifier circuit for amplifying a video signal and adding a bias voltage to the same and an output circuit for amplifying power of the signal that is delivered from the preamplifier circuit, wherein

a level of the bias voltage that is applied by the preamplifier circuit is changed in accordance with contents of the video signal, wherein the level of the bias voltage is changed in accordance with the video signal that is either a video signal related to a still image or a video signal related to a moving image.

Claim 3 (Previously Presented): A video signal circuit comprising:

a preamplifier circuit for amplifying a supplied video signal and adding a bias voltage to the same;

an output circuit for amplifying power of the signal that is delivered from the preamplifier circuit; and

a variable bias circuit for changing a level of the bias voltage that is added in the preamplifier circuit in accordance with the video signal that is either a video signal related to a still image or a video signal related to a moving image.

Claim 4 (Original): The video signal circuit according to claim 3, wherein the output circuit amplifies power of the signal that is delivered from the preamplifier circuit and inverts the same.

Claim 5 (Original): The video signal circuit according to claim 3, wherein the preamplifier circuit has a variable gain that is changed within the maximum amplitude of the output signal in accordance with the change of the bias voltage by the variable bias circuit.

Claim 6 (Canceled).

Claim 7 (Previously Presented): A display device for displaying images on a screen in accordance with a video signal, comprising:

a preamplifier circuit for amplifying a supplied video signal and adding a bias voltage to the same;

an output circuit for amplifying power of the signal that is delivered from the preamplifier circuit; and

a variable bias circuit for changing a level of the bias voltage that is added in the preamplifier circuit in accordance with an adjustment signal, wherein the variable bias circuit increases the bias voltage if the video signal relates to a still image and decreases the bias voltage if the video signal relates to a moving image.

Claim 8 (Previously Presented): A display device for displaying images on a screen in accordance with a video signal, comprising:

a preamplifier circuit for amplifying a supplied video signal and adding a bias voltage to the same;

an output circuit for amplifying power of the signal that is delivered from the preamplifier circuit;

a variable bias circuit for changing a level of the bias voltage that is added in the preamplifier circuit in accordance with an adjustment signal; and

a reception circuit for receiving a video type signal for discriminating whether the video signal relates to a still image or to a moving image, wherein

the variable bias circuit uses the video type signal as the adjustment signal for changing the level of the bias voltage.

Claim 9 (Previously Presented): The display device according to claims 7 or 8, further comprising an operation portion for adjusting the level of the bias voltage, wherein

the variable bias circuit uses a signal from the operation portion as the adjustment signal for changing the level of the bias voltage.

Claim 10 (Original): A computer system comprising a computer body and a display device for displaying images on a screen in accordance with a video signal generated in the computer body, comprising:

a video signal circuit including

a preamplifier circuit for amplifying the video signal and adding a bias voltage to the same,

an output circuit for amplifying power of the signal that is delivered from the preamplifier circuit,

a reception circuit for receiving a video type signal related to contents of the video signal, and

a variable bias circuit for changing a level of the bias voltage that is added in the preamplifier circuit in accordance with the video type signal;

a video detection portion for generating the video type signal; and

a transmission portion for transmitting the generated video type signal to the reception circuit.

Claims 11-14 (Cancelled).